

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Reducing Coupling Effect on Reference Voltages When Output Buffers Implemented with Low Voltage Transistors Generate High Voltage Output Signals

Submission Type : Utility Patent Filing

Application Number:

10/710874



EFS ID:

66284

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Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fuction call.

First Named Applicant: Rajat CHAUHAN

Attorney Docket Number: TI-38154

Timestamp: 2004-08-10 06:42:42 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-fee-sheet	txnefiling-usfees.xml	2664	2004-08-10
us-fee-sheet	us-fee-sheet.xml	24912	2004-08-10
us-fee-sheet	us-fee-sheet.dtd	11069	2004-08-10
us-ids	txnefiling-usidst.xml	821	2004-08-10
us-ids	us-ids.dtd	7763	2004-08-10
us-ids	us-ids.xml	12026	2004-08-10
us-request	txnefiling-usrequ.xml	5986	2004-08-10
us-request	us-request.dtd	19064	2004-08-10
us-request	us-request.xml	33300	2004-08-10
application-body	applitxn100-trans.xml	49363	2004-08-10
application-body	us-application-body.xml	83497	2004-08-10
application-body	application-body.dtd	49498	2004-08-10
application-body	wipo.ent	4956	2004-08-10

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application-body	isoamsb.ent	3988	2004-08-10
application-body	isoamsc.ent	1460	2004-08-10
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application-body	isoamsr.ent	7073	2004-08-10
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application-body	figtxn100_3.tif	11320	2004-08-10
application-body	figtxn100_4.tif	7796	2004-08-10
application-body	figtxn100_5.tif	8648	2004-08-10
application-body	figtxn100_6.tif	7920	2004-08-10
us-declaration	decltxn100.tif	62716	2004-08-10
package-data	txnefiling-pkda.xml	10071	2004-08-10
package-data	package-data.dtd	27025	2004-08-10
package-data	us-package-data.xsl	19263	2004-08-10
Total files size		689392	

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